

**UCC Library and UCC researchers have made this item openly available.
 Please [let us know](#) how this has helped you. Thanks!**

Title	Linear model-based testing of ADC nonlinearities
Author(s)	Wegener, Carsten; Kennedy, Michael Peter
Publication date	2004-01
Original citation	Wegener, C., Kennedy, M.P., 2004. Linear model-based testing of ADC nonlinearities. IEEE Transactions on Circuits and Systems I: Regular Papers, 51(1), pp. 213- 217. doi: 10.1109/TCSI.2003.821281
Type of publication	Article (peer-reviewed)
Link to publisher's version	http://dx.doi.org/10.1109/TCSI.2003.821281 Access to the full text of the published version may require a subscription.
Rights	©2004 IEEE. Personal use of this material is permitted. However, permission to reprint/republish this material for advertising or promotional purposes or for creating new collective works for resale or redistribution to servers or lists, or to reuse any copyrighted component of this work in other works must be obtained from the IEEE.
Item downloaded from	http://hdl.handle.net/10468/161

Downloaded on 2021-09-27T09:39:29Z

- [9] C. K. Ong, J. L. Huang, and K.-T. Cheng, "Testing second-order delta-sigma modulator using pseudorandom pattern," in *Proc. Int. Mixed-Signal Testing Workshop*, June 2001, pp. 55–71.
- [10] C. K. Ong and K. T. Cheng, "Self-testing second-order delta-sigma modulators using digital stimulus," in *Proc. VLSI Test Symp.*, Apr. 2002, pp. 237–246.
- [11] C. K. Ong, K. T. Cheng, and L. Wang, "delta-sigma modulator based mixed-signal BIST architecture for SoC," in *Proc. Asia and South Pacific Design Automation Conf.*, Jan. 2003, pp. 669–674.
- [12] S. Norsworthy, R. Schreier, and G. Temes, *delta-sigma Data Converters: Theory, Design, and Simulation*. New York: IEEE Press, 1996.
- [13] Data Converters. [Online]. Available: <http://www.ti.com/sc/docs/products/index.htm>
- [14] A-To-D. [Online]. Available: <http://www.linear.com/prod/pft.html>
- [15] J. Candy, "A use of double integration in sigma delta modulation," *IEEE Trans. Commun.*, vol. COM-33, pp. 249–258, Mar. 1985.
- [16] G. Lainey, R. Saintlaurens, and P. Senn, "Switched-capacitor second-order noise-shaping coder," *Electron. Lett.*, pp. 149–150, Feb. 1983.
- [17] J. Candy and G. Temes, *Oversampling delta-sigma Data Converters: Theory, Design, and Simulation*. New York: IEEE Press, 1992.
- [18] O. Feely, "Nonlinear dynamics of chaotic double-loop sigma-delta modulation," in *Proc. Int. Symp. Circuits and Systems*, May 1994, pp. 101–104.
- [19] E. Dijkstra, O. Nys, C. Pigué, and M. Degrauwe, "On the use of modulo arithmetic comb filters in sigma delta modulators," in *Proc. Int. Conf. Acoustics Speech and Signal Processing*, Apr. 1988, pp. 2001–2004.

Linear Model-Based Testing of ADC Nonlinearities

Carsten Wegener and Michael Peter Kennedy

Abstract—In this brief, we demonstrate the procedures of linear model-based testing for the example of a 12-b Nyquist-rate analog-to-digital converter (ADC). In a production test environment, we apply this technique to two wafer lots of devices, and we establish that the model is robust with respect to its ability to reduce the uncertainty of the test outcome. Reducing this uncertainty is particularly beneficial for higher resolution devices, for which measurement noise increasingly corrupts the measured "signal" that is the nonlinearity of the device under test.

Index Terms—Linear modeling, noise-induced test uncertainty, specification test, test cost reduction.

I. INTRODUCTION

Testing the static linearity specifications of an N -bit ADC requires one to estimate the integral nonlinearity (INL) at each of the 2^N converter codes. Traditionally, this estimate is obtained from an all-codes measurement of the INL characteristic.

When developing a production test for an ADC, a tradeoff between the measurement uncertainty and the data acquisition time has to be

Manuscript received January 15, 2003; revised October 14, 2003. This work was supported by Enterprise Ireland and Analog Devices B.V. This paper was recommended by Guest Editors A. Rodríguez-Vázquez, F. Mediero, and O. Feely.

The authors are with the Department of Microelectronic Engineering, University College Cork, Cork, Ireland (e-mail: carsten@ee.ucd.ie).

Digital Object Identifier 10.1109/TCSI.2003.821281

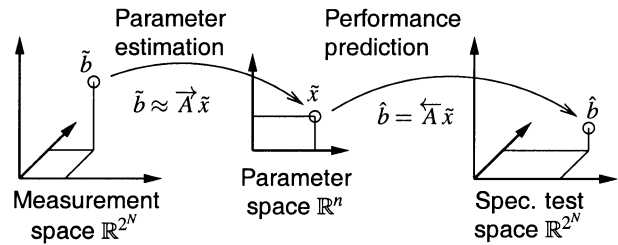


Fig. 1. Linear model-based testing of an N -bit converter: take a (noisy) measurement \tilde{b} of the device characteristic to obtain the least-squares estimate \tilde{x} of the model parameter vector that is used to predict \hat{b} , an approximation of the unknown noise-free device characteristic $b \in \mathbb{R}^{2^N}$.

performed. For an uncertainty Δ prescribed as a fraction of the least significant bit (LSB), the IEEE Standard 1241 [1] provides the following relationship:¹

$$N_s \sim \frac{1}{\Delta^2} \quad (1)$$

between the desired value of Δ and the number N_s of converter samples to be acquired. Thus, in order to halve the uncertainty Δ , the number of acquired samples needs to be quadrupled, which, for high-resolution parts, soon exhausts the typical test time budget [3].

Linear model-based testing has been proposed as an alternative approach to lower the test uncertainty without the need to increase the number of acquired samples [4]. An overview of this approach is depicted in Fig. 1. The all-codes INL measurement result, denoted by a vector $\tilde{b} \in \mathbb{R}^{2^N}$ is used to estimate the model parameter vector $\tilde{x} \in \mathbb{R}^n$, where n denotes the number parameters in the model. Based on the estimated model parameter vector \tilde{x} , the INL characteristic of the device is predicted as $\hat{b} \in \mathbb{R}^{2^N}$.

Note that the estimate \tilde{x} is obtained by solving a linear system of 2^N equations in n unknowns and, in the (typical) case of $n < 2^N$, the solution is obtained in the least-squares sense which provides the noise-reduction capability of model-based testing. Thus, the uncertainty of the predicted all-codes INL characteristic \hat{b} can be lower than the uncertainty of the measured characteristic \tilde{b} .

Reducing the uncertainty of the test outcome by model-based testing comes at a computational premium for estimating the model parameters and predicting the all-codes INL. In the $N = 12$ -b case study presented in the remainder of this brief, the uncertainty is, by a factor of four, reduced for an additional computation time equivalent to half the data acquisition time. Achieving this reduction in uncertainty of the test outcome without model-based testing, one would need to increase the number of acquired samples by a factor of 16 based on (1).

The contributions of this brief are as follows:

- we quantify the influence of measurement noise on the test outcome and demonstrate the capability of the linear model to reduce this influence;
- we distinguish between contributions to the uncertainty of the predicted INL characteristic by the model's lack-of-fit and by the measurement noise;
- we develop an approach to reduce the dominant noise-induced contribution; and
- we verify the robustness of the model-based approach in a production test environment.

¹For the 12-b test vehicle used here, we have verified this relationship experimentally as reported in [2].

II. TEST UNCERTAINTY AND LINEAR MODELING

As the aim of model-based testing is to reduce the uncertainty of the test outcome, we first quantify the noise-induced uncertainty associated with the test measurements. Then we build a linear model of the manufacturing process-induced INL-causing mechanisms. This model allows us to predict a device's all-codes INL characteristic from a noisy test measurement. Finally, we quantify the uncertainty associated with this prediction and compare this uncertainty with the previously determined measurement uncertainty.

A. Quantifying Test Uncertainties

Let B' denote a set of test characteristics, where $b' \in B'$ is a column vector whose elements represent the INL characteristic determined for a converter under test; thus, for an $N = 12$ -b analog-to-digital converter (ADC), $b' \in \mathbb{R}^{4096}$. Furthermore, let B denote a set of reference characteristics with $b \in B$ being the noise-free characteristic corresponding to $b' \in B'$.

A performance test for a min/max-type data sheet specification limit b_{\pm} rejects all devices for which

$$\|b'\|_{\infty} + \Delta > b_{\pm} \quad (2)$$

is determined. With a guard-band [5] of $\Delta \geq \|b' - b\|_{\infty}$, this test implies that the device reference characteristic b meets the data sheet performance specification $\|b\|_{\infty} < b_{\pm}$. In the following, we quantify the uncertainty Δ based on

$$e_{\max}(B', B) = \text{mean}_{(b', b) \in (B', B)} (\|b' - b\|_{\infty}) + 3 \cdot \text{std}_{(b', b) \in (B', B)} (\|b' - b\|_{\infty}). \quad (3)$$

The initial test implementation produces a histogram of 2^{18} converted samples applied to the 12-b ADC under test. From this histogram, the INL characteristic $\tilde{b} \in \mathbb{R}^{4096}$ is determined as described in [6]. In order to obtain a reference characteristic b for the tested device, we perform a second measurement with the number of histogrammed samples increased by a factor of two hundred.² For a sample set of $w = 191$ devices, we measure the test and reference characteristics in order to form the columns of two matrices \tilde{B} and B , respectively. This notation allows us to determine $e_{\max}(\tilde{B}, B)$ which characterizes the measurement uncertainty, denoted as $\tilde{e}_{\max} = 260$ mLSB, for the given measurement setup.

Note that the larger the test uncertainty Δ , the more devices which meet the data sheet performance limits $\|b\|_{\infty} < b_{\pm}$ are rejected based on (2). Thus, it is desirable to reduce the test uncertainty Δ which, in the case of a test being based on the measured characteristic \tilde{b} , evaluates to $\tilde{e}_{\max} = 260$ mLSB. In order to halve the value of \tilde{e}_{\max} , the number of samples in the histogram needs to be quadrupled, which is costly in terms of test time. An alternative approach to reducing the test uncertainty is linear model-based testing described in the following subsection.

B. Linear Modeling

The nonlinearity characteristic of a device is approximated as follows:

$$b \approx Ax + b^0 \quad (4)$$

where b^0 denotes the "nominal" device characteristic, the deviation from which is modeled as a weighted sum of the columns of the model

²Note that, by increasing the number of samples two-hundred fold, the measurement uncertainty reduces, according to (1), by a factor of more than 14 and is thus negligible compared to the uncertainty associated with \tilde{b} .

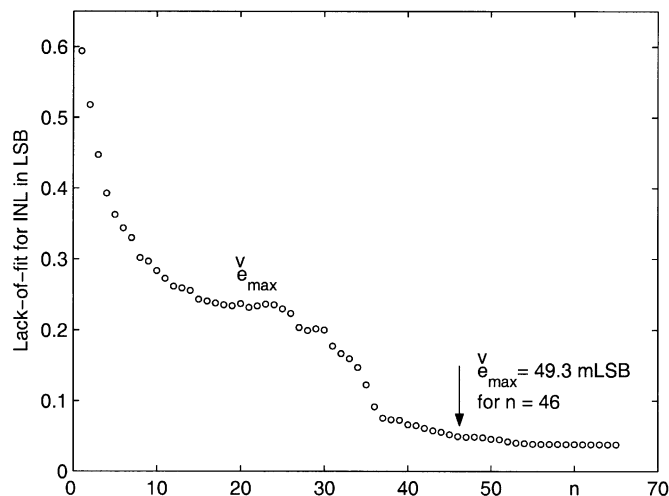


Fig. 2. Lack-of-fit \tilde{e}_{\max} versus model order n evaluated over the set of modeling devices B ; $\tilde{e}_{\max} = 49.3$ mLSB for $n = 46$.

matrix A . The weights are the elements of the model parameter vector x .

The nominal characteristic b^0 is determined as the average of the reference characteristics contained in the sample set of devices, i.e., b^0 denotes the row-mean of the matrix B . For convenience, we form a matrix B^0 of the same shape as B , but whose columns represent the nominal device characteristic b^0 .

With this notation, the model matrix A is derived from the first n columns of U which is a result of singular value decomposition (SVD [7])

$$(B - B^0) = U \Sigma V^T. \quad (5)$$

This choice of the model matrix minimizes, for a particular value of n , the lack-of-fit in the rms norm [7]. However, we are testing for min/max-type specifications and thus the required model order n should be determined based on the lack-of-fit in the maximum-norm $\|\cdot\|_{\infty}$.

To determine the model's fitted characteristic \tilde{b} for a device characteristic $b \in B$, we derive the model matrix A from $B \setminus \{b\}$, solve (4) in the least-squares sense for $x \in \mathbb{R}^n$, and compute

$$\tilde{b} = Ax + b^0. \quad (6)$$

Iterating this delete-one-cross-correlation procedure [8] for all the devices $b \in B$, we obtain a matrix \tilde{B} of fitted characteristics which allows us to determine the lack-of-fit $\tilde{e}_{\max} = e_{\max}(\tilde{B}, B)$.

As the lack-of-fit depends on the model order n , we graph in Fig. 2 the value of \tilde{e}_{\max} versus n . The plot shows that, with increasing model order n , the lack-of-fit decreases as the model is increasingly able to represent the INL-causing error mechanisms. However, for $n > 46$, the lack-of-fit levels off; this leads us to choose $n = 46$, for which \tilde{e}_{\max} evaluates to 49.3 mLSB.

In production testing, the model parameter vector can be estimated by solving

$$\tilde{b} \approx A\tilde{x} + b^0 \quad (7)$$

in the least-squares sense for $\tilde{x} \in \mathbb{R}^n$. The predicted device characteristic $\tilde{b} = A\tilde{x} + b^0$ is an approximation of b , the uncertainty of which we refer to as "prediction error" $\tilde{e}_{\max} = e_{\max}(\tilde{B}, B)$. For $n = 46$, this error evaluates to 81 mLSB, which establishes the potential of model-based testing to reduce the measurement uncertainty

$\hat{e}_{\max} = 260$ mLSB to the smaller uncertainty \hat{e}_{\max} associated with the model's prediction.

III. REDUCING THE PREDICTION ERROR

There are two primary contributions to the model's prediction error \hat{e}_{\max} : one is the model's lack-of-fit \tilde{e}_{\max} and the other is the noise-induced uncertainty of the estimated model parameter vector \tilde{x} . In this section, we develop an approach to reducing the dominant noise-induced contribution.

A. Prediction Error Contributions

For a device, the prediction error is characterized by $(\hat{b} - b)$, which we can also write as

$$\hat{b} - b = \underbrace{(\hat{b} - \tilde{b})}_{=A(\tilde{x}-x)} + \underbrace{(\tilde{b} - b)}_{\text{lack-of-fit}}. \quad (8)$$

From this notation, it becomes obvious that the prediction error is due to two contributions: one from the lack-of-fit and another from the uncertainty of the model parameter estimate \tilde{x} . Using (4) and (7), we determine that this uncertainty

$$(\tilde{b} - b) \approx A(\tilde{x} - x) \quad (9)$$

is merely due to the measurement noise contribution $(\tilde{b} - b)$.

In the following, we refer to $\tilde{x} = (\tilde{x} - x)$ as the noise-induced uncertainty of the model parameter estimate which gives rise to the noise-induced prediction error $\tilde{e}_{\max} = e_{\max}(A\tilde{X}, 0)$, with \tilde{X} being the solution of $(\tilde{B} - B) \approx A\tilde{X}$. For the model of order $n = 46$ derived previously, the value of \tilde{e}_{\max} is 67 mLSB, which is larger than $\tilde{e}_{\max} = 49.3$ mLSB and, therefore, dominates the model's prediction error $\hat{e}_{\max} = 81$ mLSB.

An obvious way of reducing the noise-induced prediction error is to reduce the measurement uncertainty \tilde{e}_{\max} . This approach is costly in terms of test time, however, due to the required increase in the number of acquired samples. An alternative is to adapt the model in order to reduce the measurement noise-induced uncertainty of the model parameter estimate.

B. Measurement Noise Influence

When estimating the model parameter vector by solving (7) in the least-squares sense, the assumption is that the measurement noise-induced INL values are independent and identically distributed. In this case, the least-squares solver performs optimally in reducing the measurement uncertainty $(\tilde{b} - b)$ to the uncertainty $(\tilde{x} - x)$ of the model parameter estimate.

This reduction becomes explicit in (9), which we can write for all devices $b \in B$ as follows: $(\tilde{B} - B) \approx A\tilde{X}$. In order to test the validity of the Gaussian noise assumption on $(\tilde{B} - B)$, we perform SVD as follows:

$$(\tilde{B} - B) = \tilde{U}\tilde{\Sigma}\tilde{V}^T \quad (10)$$

and we denote the singular values, i.e., the diagonal elements of $\tilde{\Sigma}$, as $\tilde{\sigma}_1, \tilde{\sigma}_2, \dots, \tilde{\sigma}_w$. The Gaussian-noise assumption leads to an estimate $\tilde{\sigma}'_1$ for the first (and largest) singular value. With $m = 4096$ and $w = 191$ denoting the number of rows and columns of $(\tilde{B} - B)$, respectively, the estimate $\tilde{\sigma}'_1$ is given in [9] as follows:

$$\tilde{\sigma}'_1 = \tilde{\sigma} \left(1 + \sqrt{\frac{w}{m}} \right) \quad \text{with } \tilde{\sigma} = \sqrt{\frac{1}{w} \sum_{i=1}^w \tilde{\sigma}_i}. \quad (11)$$

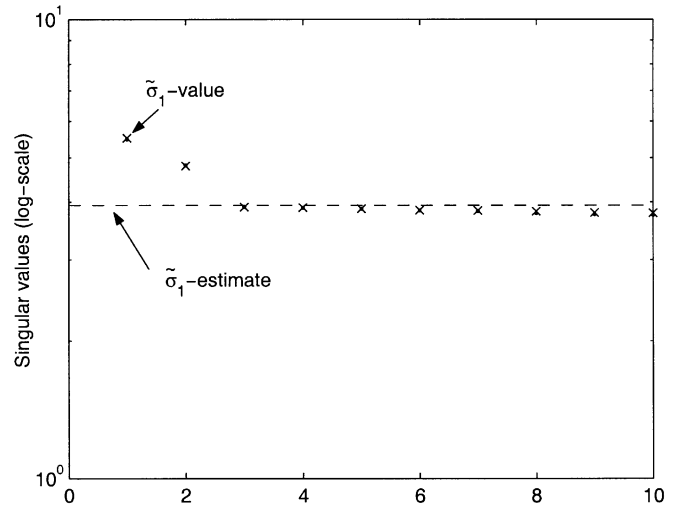


Fig. 3. First ten singular values $\tilde{\sigma}_i$ of the measurement noise (marked by crosses) and estimated singular value $\tilde{\sigma}'_1$ for pure Gaussian noise assumption (marked by horizontal dashed line).

In Fig. 3, we show the first ten singular values of the measurement noise matrix $(\tilde{B} - B)$ and indicate by a horizontal dashed line the estimate $\tilde{\sigma}'_1$ of the first singular value.

That the first two singular values $\tilde{\sigma}_1$ and $\tilde{\sigma}_2$ are significantly above the maximum singular value $\tilde{\sigma}'_1$ expected for the Gaussian measurement noise assumption indicates that the elements in $(\tilde{b} - b)$ are not independent; this degrades the noise reduction achievable when solving (9). In the following, we present a method to whiten the noise by first estimating and subsequently removing these dependencies from $(\tilde{b} - b)$ before estimating the model parameters using the *weighted* least-squares method [10].

C. Whitening Measurement Noise

Using SVD in (10), we can extract the two components in the measurement noise matrix $(\tilde{B} - B)$ for which the singular values are above $\tilde{\sigma}'_1$. The associated singular vectors, i.e., the first two columns of \tilde{U} , are shown in the top and middle plot of Fig. 4.

The columns of \tilde{U} that are associated with singular values which are significantly above the noise floor are abbreviated by D . With D being orthogonal by construction, D^T is the pseudo-inverse of D . Thus, we can extract from a given measurement noise vector $(\tilde{b} - b)$ the non-Gaussian noise components by computing $D(D^T(\tilde{b} - b))$. Subtracting these non-Gaussian components from $(\tilde{b} - b)$ results in

$$b^* = (\tilde{b} - b) - D(D^T(\tilde{b} - b)) \quad (12)$$

whose elements are independent.

The standard deviation of the elements in b^* versus code is shown in the bottom part of Fig. 4. The bow-shape can be explained by the pdf of the sinewave signal histogrammed for deriving the INL characteristics of the devices. At mid-scale of the sinewave, the pdf assumes its minimum, causing a maximum of the measurement uncertainty, i.e., the standard deviation of the noise. At either end of the code range, the pdf of the sinewave approaches its maximum and, thus, minimizes the measurement noise-induced uncertainties.

The method of *weighted* least-squares [7] equalizes the standard deviation at each code before estimating the least-squares solution for the model parameters. For the i th element in b^* , we assign

$$s_i = \begin{cases} 1, & \text{if } \text{std}(b_i^*) = 0 \\ \frac{1}{\text{std}(b_i^*)}, & \text{otherwise} \end{cases} \quad (13)$$

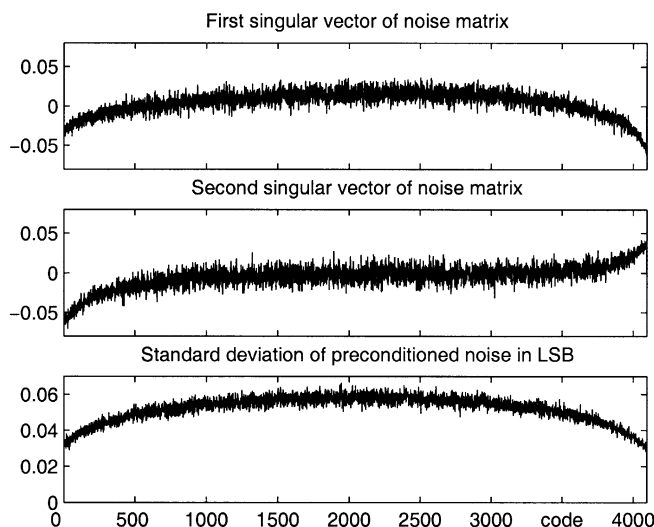


Fig. 4. First two singular vectors of measurement noise matrix ($\bar{B} - B$) and standard deviation of \hat{b}^* versus code.

and denote $S = \text{diag}(s_1, s_2, \dots, s_{4096})$. Each element in b^* is scaled by s_i , i.e., we multiply $S \cdot b^*$ with the result that the standard deviations of the elements in the column vector ($S \cdot b^*$) are equalized. Thus, the vector

$$S \cdot ((\bar{b} - b) - D(D^T(\bar{b} - b))) \quad (14)$$

approximates the white-noise assumption of the least-squares method more closely than the measurement noise vector $(\bar{b} - b)$.

The model extraction method (5) needs to be extended to account for the impact on the model parameter estimation of our method of whitening the measurement noise. Instead of (5), we perform SVD as follows:

$$\begin{pmatrix} B - B_0 \\ S \cdot [(B - B_0) - D(D^T(B - B_0))] \end{pmatrix} = U\Sigma V^T \quad (15)$$

and extract the model that is an $(8192 \times n)$ -matrix from the first n columns of U . From this matrix, we form two matrices \overleftarrow{A} and \overrightarrow{A} by taking the top and bottom 4096 rows, respectively.

The noise-induced prediction error is computed as $\bar{b} = \overleftarrow{A} \bar{x}$ based on \bar{x} , the least-squares solution of

$$S \cdot [(\bar{b} - b) - D(D^T(\bar{b} - b))] \approx \overrightarrow{A} \bar{x}. \quad (16)$$

For $n = 46$, the noise-induced prediction error evaluates to $\bar{e}_{\max} = 43$ mLSB, which is significantly lower than the value of 67 mLSB we obtained without whitening the noise.

On the production line, a device measurement is performed, denoted by \tilde{b} . The model parameter vector \tilde{x} is estimated by solving

$$S \cdot [(\tilde{b} - b_0) - D(D^T(\tilde{b} - b_0))] \approx \overrightarrow{A} \tilde{x} \quad (17)$$

in the least-squares sense. Then, the predicted device response $\hat{b} = \overleftarrow{A} \tilde{x} + b_0$ is computed.

Since the noise-induced uncertainty of the estimated model parameter vector \tilde{x} is reduced, the prediction error reduces to $\hat{e}_{\max} = 64$ mLSB, which constitutes a 20% improvement compared to the 81 mLSB for the prediction error without whitening the measurement noise.

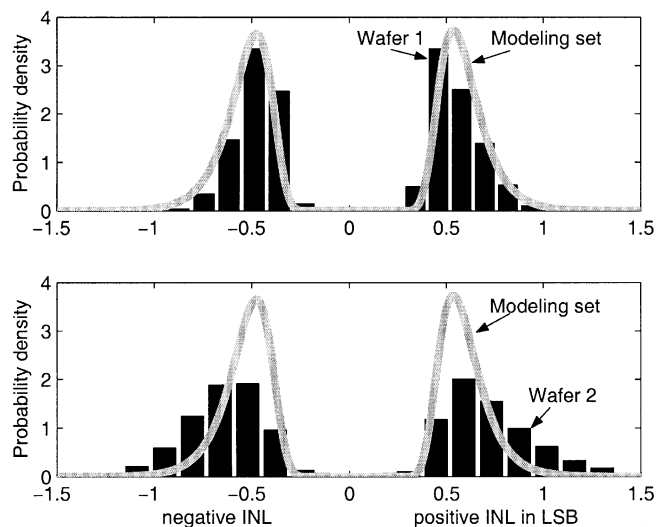


Fig. 5. Probability density versus INL performance for wafers 1 and 2 (shown as histograms) and for the modeling set (shown as a solid curve).

IV. IMPLEMENTATION IN PRODUCTION TEST

A. Test Program Implementation

For the 12-b ADC, a test program was available that performed the linearity measurement based on histogramming 22 periods of a low-frequency sine wave. Based on this measured INL characteristic, denoted by a vector $\tilde{b} \in \mathbb{R}^{4096}$, the predicted device characteristic \hat{b} is computed. Then, the worst-case INL value, i.e., $\|\hat{b}\|_{\infty}$, is determined and, in order to pass or fail the device under test, the test condition (2) is applied as follows:

$$\|\hat{b}\|_{\infty} + \Delta < b_{\pm} \quad (18)$$

with $\Delta = \hat{e}_{\max} = 64$ mLSB accounting for the prediction error.

The computations required to derive \hat{b} from a measurement \tilde{b} add, in our implementation, half the time required for taking the measurement \tilde{b} . Equating the measurement time to T , the test time using the model-based approach is $1.5T$, which achieves a reduction of the test uncertainties to $\hat{e}_{\max} = 64$ mLSB.

When targeting the same reduction of the test uncertainty by accumulating more samples in the histogram, the measurement time would increase by a factor of $(260/64)^2 \approx 17$, thus resulting in a total test time of $17T$. With our implementation of model-based testing, we obtain a speed-up by a factor of $17T/1.5T \approx 11$ for the same test accuracy of $\Delta = 64$ mLSB.

B. Verification of Model Performance

We have applied the modified test program to two wafer lots of approximately 8000 devices each. In each device lot, we found six devices whose measured characteristic \tilde{b} violated the customer INL specification limits $b_{\pm} = 1.5$ LSB by more than $\bar{e}_{\max} = 260$ mLSB, thus failed the test even before the linear model was applied.

Excluding devices that grossly violate the INL specifications, we show in Fig. 5 histograms of the probability density versus the exhibited most positive and negative INL values. We can compare these histograms to the pdf we obtain for the modeling set by overlaying this pdf as a solid curve in the figure. This comparison indicates that wafer 1 has an INL performance spread close to the modeling set, while the histogram for wafer 2 indicates a significantly wider spread of the exhibited INL values.

That wafer 2 is “different” from wafer 1 is also indicated by the results for another test that was performed on the devices. In contrast

TABLE I
LACK-OF-FIT $\tilde{\epsilon}_{\max}^{46}$ AND PREDICTION ERROR $\hat{\epsilon}_{\max}^{46}$ FOR A
MODEL OF ORDER $n = 46$ AND REFERENCE MEASUREMENTS
 b USING p SINEWAVE PERIODS

	modeling set $p = 4400$	modeling set $p = 550$	wafer 2 subset $p = 550$
$\tilde{\epsilon}_{\max}^{46}$	49 mLSB	72 mLSB	68 mLSB
$\hat{\epsilon}_{\max}^{46}$	64 mLSB	82 mLSB	85 mLSB

to wafer 1, half of the devices from wafer 2 failed the SINAD test [1], a linearity test performed under dynamic conditions, i.e., with a high-frequency sinewave input.

The question arises whether the manufacturing process excursion experienced by wafer 2 degrades the model performance we derived earlier for the modeling set of devices. Such a degradation can be caused by error mechanisms that were not present or were of minor significance in the set of devices used for model building [11].

In order to verify that model performance is sustained, we took a subset of 203 devices from wafer 2 and measured the INL characteristics twice: once at normal production accuracy, denoted by \tilde{b} , and once with twenty-five times as many histogrammed samples to derive the reference characteristic b . From these measurements, we can estimate the model's lack-of-fit and the prediction error, the results of which are summarized in Table I.

For the prediction error, we determined in Section III-C that $\hat{\epsilon}_{\max} = 64$ mLSB. From the measurements of the 203 devices from wafer 2, we determine this prediction error to be 85 mLSB. The gap between these two results can be explained by the uncertainties of the measurements used as the reference b .

Recall that, in the model building stage, the reference measurement b was obtained by histogramming 4400 periods of the sinewave, i.e., 200 times as many periods as used in production to obtain \tilde{b} . Since the measurements for the 203 devices from wafer 2 were taken during the production test run, measurement time was limited, and only 550 sinewave periods were histogrammed. The smaller number of histogrammed samples leads to significant noise-induced uncertainties in the reference b which tends to increase the numbers we obtain for the lack-of-fit $\tilde{\epsilon}_{\max}$ and the prediction error $\hat{\epsilon}_{\max}$, respectively.

In order to estimate the magnitude of this increase, we determined "reference" characteristics for the modeling set of devices by histogramming 550 periods only. Recomputing the prediction

error $\hat{\epsilon}_{\max}$ for the model yields 82 mLSB. This value is comparable to the 85 mLSB we obtain for the 203 devices from wafer 2 and, therefore, the results in Table I do not suggest a degradation of the model performance for wafer 2. This sustained model performance is achieved despite the fact that wafer 2 experienced an abnormal manufacturing process excursion that led to a 50% drop in yield.

V. CONCLUSION

For the example of a 12-b Nyquist-rate ADC, we establish that the uncertainty of the test outcome can be reduced by model-based testing. We implemented the test procedure in an industrial test environment and achieve this reduction in one eleventh of the test time it takes with the traditional approach of increasing the number of acquired converter samples. Furthermore, by applying the model-based technique to two wafer lots of devices, we established that the model is robust to significant manufacturing process excursions.

REFERENCES

- [1] *IEEE Standard for Terminology and Test Methods for Analog-To-Digital Converters*, IEEE Std. 1241-2000, Dec. 2000.
- [2] C. Wegener and M. P. Kennedy, "Implementation of model-based testing for medium- to high-resolution nyquist-rate ADC's," in *Proc. ITC, Int. Test Conf.*, Baltimore, MD, Oct. 2002, pp. 851–860.
- [3] M. Burns and G. W. Roberts, *An Introduction to Mixed-Signal IC Test and Measurement*, 1st ed, ser. Oxford Series in Electrical and Computer Engineering. New York: Oxford Univ. Press, 2001.
- [4] P. Capofreddi and B. Wooley, "The use of linear models in A/D converter testing," *IEEE Trans. Circuits Syst. I*, vol. 44, pp. 1105–1113, Dec. 1997.
- [5] R. Williams and C. Hawkins, "The economics of guardband placement," in *Proc. ITC, Int. Test Conf.*, 1993, pp. 218–224.
- [6] J. Doernberg, H. Lee, and D. Hodges, "Full speed testing of A/D converters," *IEEE J. Solid-State Circuits*, vol. SC-19, pp. 820–827, Dec. 1984.
- [7] G. Golub and C. van Loan, *Matrix Computations*, 3rd ed. Baltimore, MD: Johns Hopkins Univ. Press, 1996.
- [8] A. Ding and J. Hwang, "Prediction intervals, factor analysis models, and high-dimensional empirical linear prediction," *J. Amer. Statist. Assoc.*, vol. 94, no. 446, pp. 446–455, June 1999.
- [9] J. Silverstein and P. Combettes, "Large dimensional random matrix theory for signal detection and estimation in array processing," in *SSAP, Workshop on Statistical Signal and Array Processing*, Victoria, BC, Canada, Oct. 1992, pp. 276–279.
- [10] A. Koffman and T. Souders, "Application of the NIST testing strategies to a multi-range instrument," in *Proc. Measurement Science Conf.*, Pasadena, CA, Jan. 1994, p. 8.
- [11] G. Stenbakken, "Effects of nonmodel errors on model-based testing," *IEEE Trans. Instrum. Meas.*, vol. 45, pp. 384–388, Apr. 1996.