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<b>Title</b>	Mind the drain from strain: effects of strain on the leakage current of Si diodes
<b>Author(s)</b>	Murphy-Armando, Felipe; Liu, Chang; Zhao, Yi; Duffy, Ray
<b>Publication date</b>	2016-10
<b>Original citation</b>	Murphy-Armando, F., Liu, C., Zhao, Y. and Duffy, R. (2016) 'Mind the drain from strain: effects of strain on the leakage current of Si diodes', 13th IEEE International Conference on Solid-State and Integrated Circuit Technology (ICSICT) Hangzhou, China, 25-28 October. doi:10.1109/ICSICT.2016.7999046
<b>Type of publication</b>	Conference item
<b>Link to publisher's version</b>	<a href="http://dx.doi.org/10.1109/ICSICT.2016.7999046">http://dx.doi.org/10.1109/ICSICT.2016.7999046</a> Access to the full text of the published version may require a subscription.
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# Mind the drain from strain: effects of strain on the leakage current of Si diodes

Felipe Murphy-Armando<sup>1\*</sup>, Chang Liu<sup>2</sup>, Yi Zhao<sup>3</sup>, Ray Duffy<sup>1</sup>

<sup>1</sup>Tyndall National Institute, University College Cork, Ireland

<sup>2</sup>School of Electronic Science and Engineering, Nanjing University, 22 Hankou Rd, Nanjing 210093, China

<sup>3</sup>Department of Information Science and Electronic Engineering, Zhejiang University, 38 Zheda Rd, Hangzhou 310027, China

\* Email: philip.murphy@tyndall.ie

## Abstract

We present a systematic study of the impact of strain on off-state leakage current, using experimental data and ab-initio calculations. We developed new models to account for the impact of strain on band-to-band tunneling and trap-assisted tunneling in silicon. We observe that the strain can dramatically increase the leakage current, depending on the type of tunneling involved. We predict that 1% compressive strain can increase the band-to-band tunneling and Shockley Read Hall leakage currents by over 5 and 3 times, respectively.

## 1. Introduction

Strain is nowadays essential to enhance the on-state performance of modern and future CMOS technologies. [1,2] This is achieved by improving the mobility of the device channel. However, most of the strain enhancements have focused on the on-state drive current, and little study has been devoted to the effects of strain on the off-state leakage current. The off-state leakage current originates at the reverse biased drain junction via band-to-band tunneling (BBT), Shockley-Reed-Hall (SRH) and trap-assisted tunneling (TAT).

In this work we explore both theoretically and experimentally the effects of strain on the leakage current of several Si diode samples, selected to isolate the contribution from the different tunneling mechanisms. We find that compressive strain has a dramatic effect on diodes dominated by either BBT or SRH currents, leading to a 2 to over 20 times increase in leakage current per 1% strain.

## 2. Diodes and measurements

The diodes used in this study were previously fabricated [3,4] with different doping concentrations in the substrate in order to isolate different leakage mechanisms, including BBT, SRH and TAT. This enabled a closer evaluation of these reverse leakage mechanisms.

The mechanical stresses were applied using a four-point wafer bending system. Using this system, we studied the effects of both uniaxial tensile and compressive strain.

Details of the mechanical stress equipment can be found in Refs. 5 and 6.

## 3. Theory and definitions

Prior to the analysis of the experimental results, a brief review of important theory is now presented. For the sake of this discussion we assume the carriers are electrons. Generation mechanisms that lead to leakage currents are temperature and/or electric field dependent. To extract what mechanisms are dominating we need to analyze (1) the voltage dependence and (2) the temperature dependence of the leakage current. The most relevant to our study are diffusion current, direct transition thermal generation, Shockley Read Hall (SRH) generation, trap assisted tunneling (TAT), and band to band tunneling (BBT).

Diffusion current occurs when carriers, which are at a higher energy than the potential barrier, diffuse across from one side of the junction to the other. Thermal generation occurs when electrons are thermally excited, by light or heat, and make the vertical transition directly into the conduction band. SRH generation is temperature dependent, electric field dependent, and relies on the presence of deep levels in the depletion region. SRH generation occurs when an electron that is trapped in a deep level, gains energy, and climbs out of the Coulombic well. SRH dominated current is proportional to the width of the depletion region and thus  $V^{0.5}$ . TAT can be considered as SRH in the presence of an electric field, or as a combination of electron capture and tunneling through the barrier. In this case the tunneling electron uses a deep level trap in the depletion region as a stepping stone to make the transition. BBT occurs when the electric field across the junction is strong enough to propel an electron from the valence band on the p-side through the potential barrier and forbidden band gap, into the conduction band on the n-side. The

BBT current  $I_{bbt} \propto \exp\left(-\frac{E_g^{0.5}}{F_m}\right)$ , where  $E_g$  is the

silicon band gap (= 1.12 eV), and  $F_m$  is the maximum electric field.[7] In other words the probability of BBT increases if the electric field increases or if the tunneling distance decreases. Hurkx et al. reported that BBT

becomes important above a local electrical field strength of  $7 \times 10^5$  V/cm. [8]

An effective method for determining the different physical components in reverse leakage currents is to perform measurements at elevated temperatures,[9,10] as some generation mechanisms are temperature dependent while others are independent. Ideal diffusion current is proportional to  $n_i^2$ , so the temperature dependence behaviour is related to the temperature dependence of  $n_i$ , which is  $n_i \propto \exp\left(-\frac{E_g}{2kT}\right)$ .

Thus  $I_{ideal} \propto \exp\left(-\frac{E_g}{kT}\right)$ , and the activation

energy ( $E_A$ ) is expected to be close to  $E_g$ . For direct transition thermal generation  $E_A$  is also expected to be close to  $E_g$ . SRH generation is proportional to the intrinsic carrier concentration,  $n_i$ . The temperature dependence behaviour is thus related to the temperature

dependence of  $n_i$ , so  $I_{SRH} \propto \exp\left(-\frac{E_g}{2kT}\right)$ , and

$E_A$  is expected to be  $E_g/2$ . TAT is also temperature dependent and the extracted  $E_A$  of the current characteristics will indicate a trap level within the band gap. Usually this value is close to  $E_g/2$ . In general the temperature dependence of BBT is related to the temperature dependence of  $E_g/2$ . As a rule of thumb, BBT increases approximately x2 for a 100 °C increase above room temperature, so  $E_A$  is quite close to 0 eV.

To compute the leakage current vs. strain, we have modified the models by A. Schenk[11] for BBT and G.A.M. Hurkx for SRH and TAT.[12] The band structure of Si under strain is calculated using the 30-band k.p model of Rideau et al.[13] The electron-phonon coupling necessary for the BBT is calculated using Density Functional Perturbation Theory as in Refs. 14-16, and available in the code Abinit.[17,18] All the parameters required for the calculation of the current have been calculated as a function of strain, where possible (p. eg, effective masses, band structure and electronic distribution). Details of our model will be available in Ref. 19.

#### 4. Discussion

We measured and calculated the reverse bias current percent-change as a function of uniaxial stress in the (100) direction in all the samples. Figures 1 and 2 show these for samples BA2 and BA3 of Ref. 3, respectively. For such small stresses, the change in current density effected by the strain is quite large, up several 10s of percent for sample BA2.

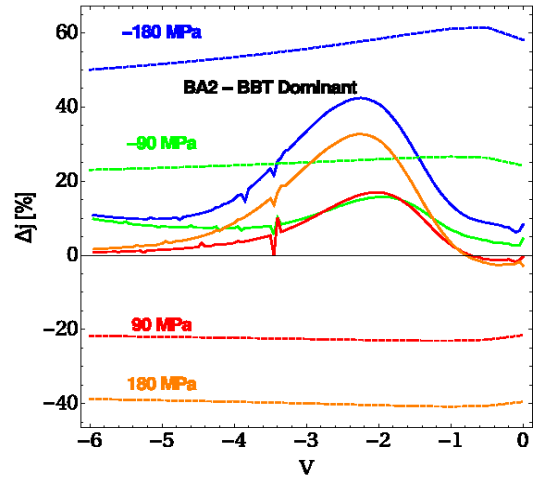


Figure 1. Current density change (in percent) vs reverse bias voltage for sample BA2 at 4 different stresses (-180, -90, 90, 180) MPa represented as (blue, green, red and orange). Solid: experiment; dashed: model.

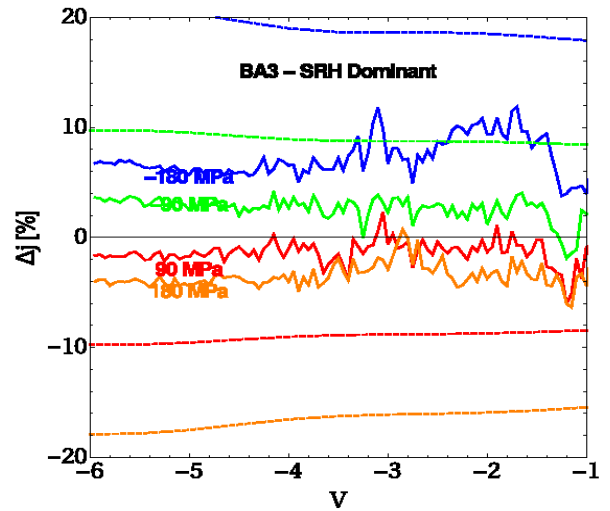


Figure 2. Current density change (in percent) vs reverse bias voltage for sample BA3 at 4 different stresses (-180, -90, 90, 180) MPa represented as (blue, green, red and orange). Solid: experiment; dashed: model.

Strain affects BBT and SRH differently, as shown in Figure 3. Notably, BBT is most sensitive to compressive stress. Under enough compressive stress, BBT can become dominant over the other tunneling mechanisms. Figure 3 also shows the predicted calculated leakage current densities at stresses normally used for enhancing the mobility of Si in transistor channels. Worryingly, the leakage current can increase by several hundred percent in both BBT and SRH dominant devices under compressive stress. Conversely, tensile stress may decrease the leakage.

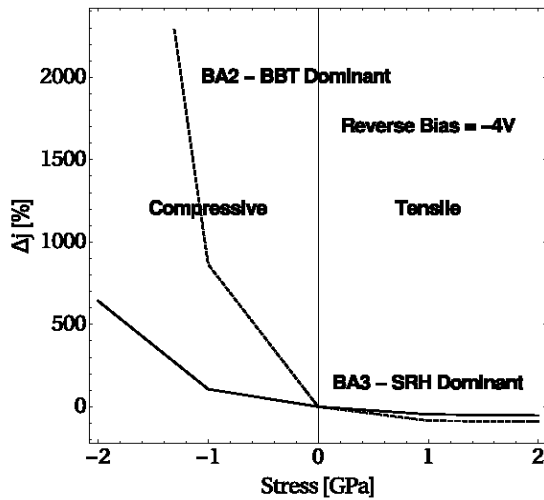


Figure 3. Prediction of the current density change of sample BA2(dashed) and BA3(solid) under high strain at a reverse bias voltage of -4V. Transport in BA1 and BA4 is dominated by BBT and SRH, respectively.

## 5. Conclusion

We have measured the effect of uniaxial stress on the leakage current of Si diodes. The samples were chosen to elicit the different leakage current mechanisms, i.e. band to band, trap assisted and Shockley-Reed-Hall tunneling. We find that strain strongly affects the leakage current for all types of tunneling. Less than a percent strain can elicit several percent change in the leakage current. The effect is most dramatic for BBT. We have also developed a model partially based on first principles calculations to explain these results, and predict the effect of commonly used strains on the leakage current of devices. We find relatively good agreement between our calculations and the measurements, and are able to explain many of the observed features. Most importantly, we predict that compressive strain can dramatically increase the leakage current, while tensile strain suppresses it.

## Acknowledgments

FMA would like to acknowledge Science Foundation Ireland for funding.

## References

- [1] Y. Song, H. Zhou, Q. Xu, J. Luo, H. Yin, J. Yan, and H. Zhong, *J. El. Mat.* 40 (2011).
- [2] E. Ungersboeck, V. Sverdlov, H. Kosina, and S. Selberherr, ICSICT-2006: 2006 8th International

- Conference on Solid-State and Integrated Circuit Technology, Proceedings, 124 (2007).
- [3] R. Duffy, A. Heringa, V. C. Venezia, J. Loo, M. A. Verheijen, M. J. P. Hopstaken, K. van der Tak, M. de Potter, J. C. Hooker, P. Meunier-Beillard, and R. Delhougne, *Solid State Electronics* 54, 243 (2010).
- [4] R. Duffy, A. Heringa, J. Loo, E. Augendre, S. Severi, and G. Curatola, *ECS Transactions* 3, 19 (2006).
- [5] W. Wu, C. Liu, J. Sun, W. Yu, X. Wang, Y. Shi, and Y. Zhao, *IEEE El. Dev. Lett.* 35, 714 (2014).
- [6] W. Wu, Y. Pu, J. Wang, X. Xu, J. Sun, Z. Yuan, Y. Shi, and Y. Zhao, *Appl. Phys. Lett.* 102, 093502 (2013).
- [7] G. A. M. Hurkx, *Solid State Electronics* 32, 665 (1989).
- [8] G. A. M. Hurkx, D. B. Klaassen, and M. P. G. Knuvers, *IEEE Trans. Electron Devices* 39, 331 (1992).
- [9] N. V. Loukianova, H. O. Folkerts, J. P. V. Maas, D. W. E. Verbugt, A. J. Mierop, W. Hoekstra, E. Roks, and A. J. P. Theuwissen, *IEEE Trans. Electron Devices* 50, 77 (2003).
- [10] A. Poyai, E. Simoen, C. Claeys, A. Czerwinski, and E. Gaubas, *Appl. Phys. Lett.* 78, 1997 (2001).
- [11] A. Schenk, *Solid State Electronics* 36, 19 (1993).
- [12] G. A. M. Hurkx, H. C. de Graaf, W. J. Kloosterman, and M. P. G. Knuvers, *IEEE Trans. Electron Devices* 39, 2090 (1992).
- [13] D. Rideau, M. Feraille, L. Ciampolini, M. Minondo, C. Tavernier, H. Jaouen, and A. Ghetti, *Phys. Rev. B* 74, 195208 (2006).
- [14] F. Murphy-Armando and S. Fahy, *Phys. Rev. B* 78, 035202 (2008).
- [15] F. Murphy-Armando and S. Fahy, *Phys. Rev. B* 86, 079903 (2012).
- [16] F. Murphy-Armando and S. Fahy, *J. App. Phys.* 110, 123706 (2011).
- [17] X. Gonze, *Phys. Rev. B* 55, 10337 (1997).
- [18] X. Gonze, B. Amadon, P.-M. Anglade, J.-M. Beuken, F. Bottin, P. Boulanger, F. Bruneval, D. Caliste, R. Caracas, M. Côté, T. Deutsch, L. Genovese, P. Ghosez, M. Giantomassi, S. Goedecker, D. Hamann, P. Hermet, F. Jollet, G. Jomard, S. Leroux, M. Mancini, S. Mazevet, M. Oliveira, G. Onida, Y. Pouillon, T. Rangel, G.-M. Rignanese, D. Sangalli, R. Shaltaf, M. Torrent, M. Verstraete, G. Zerah, and J. Zwanzig, *Computer Physics Communications* 180, 2582 (2009).
- [19] F. Murphy-Armando, C. Liu, Y. Zhao, and R. Duffy, In preparation (2016).