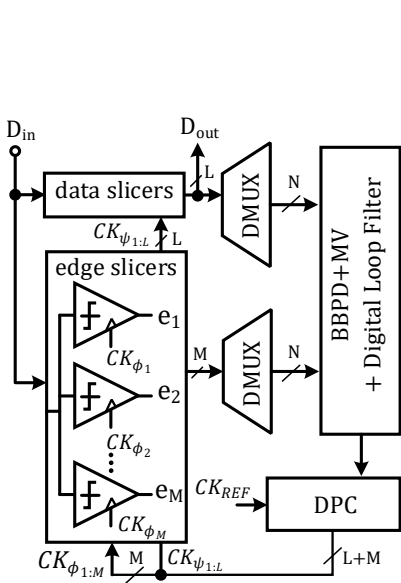




Differential
Input PAM4
data (56 Gb/s)

Differential I/Q
reference clock

Data sampling
phases

Edge sampling
phases

