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Electrical characterization of the soft breakdown failure mode in MgO layers

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The soft breakdown (SBD) failure mode in 20 nm thick MgO dielectric layers grown on Si substrates was investigated. We show that during a constant voltage stress, charge trapping and progressive breakdown coexist, and that the degradation dynamics is captured by a power-law time dependence. We also show that the SBD current-voltage (I - V) characteristics follow the power-law model $I=aV^b$ typical of this conduction mechanism but in a wider voltage window than the one reported in the past for SiO₂. The relationship between the magnitude of the current and the normalized differential conductance was analyzed. © 2009 American Institute of Physics.

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In recent papers, magnesium oxide (MgO) has been pointed out as an appealing material for use as a gate insulator in metal-oxide-semiconductor (MOS) devices both for low¹ and high² power applications. The main features of this alkaline earth oxide are: a large band gap in the range 7.3 eV (Ref. 1)–7.8 eV (Ref. 3), ensuring sufficiently large band offsets with Si and therefore low leakage current, a dielectric permittivity κ ranging from 6.7 (Ref. 4) to 10 (Ref. 3) depending on the preparation method, high thermal conductivity suitable for large power dissipation,² and significantly, its chemical inertness, which minimizes the formation of an interfacial layer when deposited on Si substrates.¹ In this letter, we have focused attention on a specific reliability aspect of MgO-Si system, which is the conduction mechanism after the occurrence of soft breakdown (SBD) events.⁵ As is commonly accepted, this failure mode arises as a consequence of the formation of a single or multiple nanoconstrictions across the oxide layer after reaching a certain threshold density of defects caused by the electrical stress. This is a localized conduction mode, independent of the device area, and has been reported to occur in a number of high- κ thin films other than SiO₂.⁶ However, contrary to the widespread opinion that this failure mechanism can only be observed in ultrathin ($t_{\text{ox}} < 6$ nm) oxides, we have detected it in MgO films with nominal $t_{\text{ox}} = 20$ nm, with t_{ox} the oxide thickness. Notably, our sample thickness almost doubles the maximum SiO₂ thickness ($t_{\text{ox}} = 12$ nm) for which SBD has been reported.⁷ In order to demonstrate that we are strictly dealing with the same charge transport mechanism, we have carried out a thorough analysis of the conduction characteristics starting with the widely accepted power-law model $I = aV^b$, where a and b are constants.⁸ This allows us to examine the limits of the validity of the referred model, identify SBD events with leakage current levels several orders of magnitude lower than those detected in the past, which in thinner oxides would have been overlooked and, finally, extend the relationship

exhibited by the fitting parameters a (I at 1V), and b ($=d \ln I / d \ln V$) previously reported for the SiO₂/Si system.

Hydrogen terminated Si surfaces were prepared by dipping n - and p -type (100) Si wafers (10^{15} cm⁻³) in a solution of 5% hydrofluoric acid (HF) for 1 min, followed by rinsing in de-ionized water, and dried using nitrogen and were then immediately loaded into the deposition system. MgO films of nominal $t_{\text{ox}} = 20$ nm with $\kappa \sim 8.1$ (equivalent oxide thickness EOT ≈ 9.5 nm) were deposited by electron beam evaporation from 99.9% MgO pellets at a rate of 0.2 Å/s, at 180 °C. The samples were capped *in situ* with 100 nm of amorphous silicon (α -Si) using a second e-beam source. For the NiSi gate process, nickel was deposited by electron beam evaporation (~ 80 nm) through a patterned resist mask followed by a lift-off process. The rapid thermal annealing is a one step process at 500 °C for 30 s in N₂. The area of the devices tested is 1.6×10^{-5} cm². Details about the SiO₂ samples with $t_{\text{ox}} \approx 4$ nm considered in this study can be found in Ref. 5.

The dielectric breakdown of the MgO layer can be induced by either ramped or constant voltage stresses. Figure 1 shows the effects of the latter type of stress on the leakage

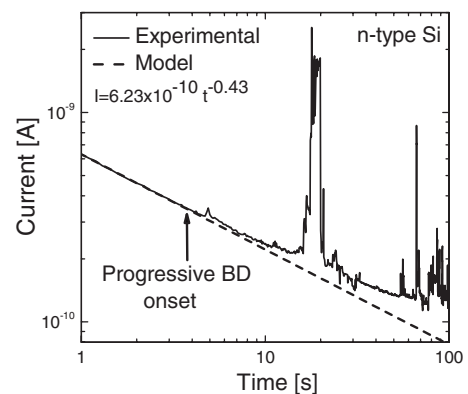


FIG. 1. Evolution of the gate leakage current under a constant voltage stress at 8 V. The solid and dashed lines correspond to experimental data and fitting results, respectively.

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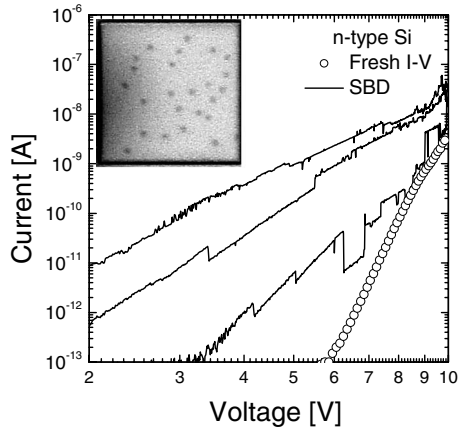


FIG. 2. Experimental current-voltage characteristics. Symbols and lines correspond to the fresh and SBD I - V characteristics, respectively. Notice the use of log-log axis in order to emphasize the power-law dependence. The maximum voltage stress is 10 V. The inset shows several SBD spots distributed over the device area.

current. Notice that the current decreases according to the empirical law $I(t) = I_0 t^{-\alpha}$, where I_0 and α are constants and t is the stress time, in agreement to what has been observed in other high- κ s such as Al_2O_3 and HfO_2 .⁹ The origin of the current decay is still unclear but, since the process is partially reversible, it might be related to transient and permanent charge trapping effects in pre-existing and induced traps. The onset of dielectric breakdown is detected as a noise increment in the I - t characteristic and this event is followed by a progressive increase of the current [progressive breakdown (PBD)]. Alternatively, SBD can be achieved by successive ramped voltage stresses as shown in Fig. 2. The major damage to the sample is caused at the end of each sweep and, in most of the cases, no current jump associated with SBD is observed, an indication of the progressive opening of the leakage spots. The switching behavior, typical of digital SBD conduction,¹⁰ is clearly observed in the second and third I - V characteristics. Contrary to what happens with ultrathin SiO_2 layers, in which SBD is only observable in a very limited voltage range because of the tunneling current that flows through the nondamaged device area,¹¹ in the present case of a 20 nm thick sample the SBD curve can be detected in a larger voltage window. The SBD I - V curves cannot be associated with a single constriction and it is more likely that many leakage sites contribute to the total current. The inset in Fig. 2 supports this assertion and shows the occurrence of several breakdown spots on the gate electrode after the stress sequence. According to our data the breakdown field for SBD in MgO is around 5–6 MV/cm which is comparable to the breakdown field in HfO_2 (4–5 MV/cm) but much lower than in SiO_2 (13–14 MV/cm).¹² Regarding the voltage dependence of the SBD curves, notice that the linear relationship between $\log(I)$ and $\log(V)$ supports the use of the power-law model. From the physical viewpoint, this model has been related to the conduction properties of nonlinear resistances networks with a distribution of percolation thresholds.¹³ However, it is worth pointing out that the model is unable to capture the experimental fact that for larger damaged areas the slope of the I - V curves becomes shallower. This trend was first observed by Okada and Taniguchi¹⁴ in SiO_2 and was thoroughly analyzed in Refs. 8 and 15 for samples subjected to electrical stress and heavy ion irradiation,

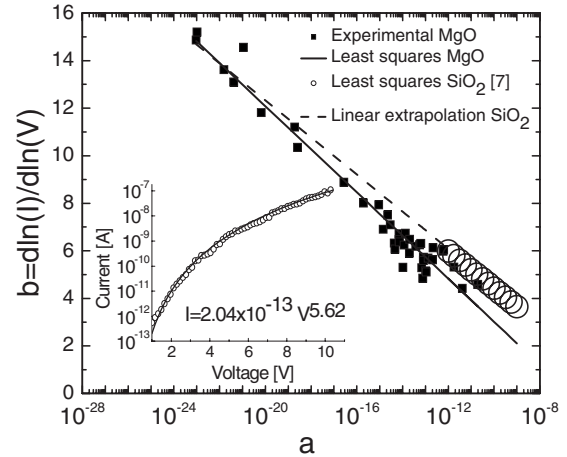


FIG. 3. Relationship between the parameters a and b in the power-law model. The circles correspond to the region of SiO_2 ($t_{\text{ox}} \approx 3.8$ – 4.3 nm) experimental data given by the equation $b = -0.78 \log(a) - 3.27$ [8]. The squares correspond to the parameters extracted from the MgO SBD I - V curves. The solid line is given by the equation $b = -0.90 \log(a) - 6.05$.

tion, respectively. The change in slope is consistent with the fact that the hard breakdown (HBD) I - V characteristic has slope close to unity,⁵ as expected for a simple resistor. This relationship between the magnitude of the current a and its normalized differential conductance b which has not been reported before for a high- κ with such a large thickness, has been explained in terms of the mesoscopic transport properties of the breakdown paths.¹⁶ Figure 3 shows the correlation between a and b obtained from a large number of I - V curves and the inset shows a typical fitting to a SBD I - V characteristic in MgO using the power-law model. The points in Fig. 3 cannot be considered as a statistically independent data ensemble since some of them were extracted from I - V s measured in the same sample after further degradation. On the contrary, in Ref. 8 the parameters were extracted from independent measurements. Even though this parameter extraction procedure can lead to uncertainty, what is remarkable is that both data sets (MgO and SiO_2) follow very similar linear trends. It is worth mentioning that thicker SiO_2 films than those considered in this study only exhibit the HBD mode so that the comparison between samples with similar large thicknesses is not possible. Figure 3 also shows that the correlation between a and b in MgO covers nearly ten orders of magnitude of the parameter a , whereas for SiO_2 this correlation holds in a narrower range. A possible interpretation for the observed correlation is that the size of the spots is strongly linked to its conducting properties, in such a way that the damaged area does not enter as a simple prefactor in the current expression as commonly assumed for standard transport mechanisms such as tunneling, Poole-Frenkel or Schottky emission. In addition, Fig. 3 shows that many of the SBD events occurring in MgO are softer than those detected in SiO_2 . This can be a matter of the sample thickness, where the magnitude of tunneling currents permits the observation of softer events, but the possibility that it is related to particular features of the conducting spots, such as the microscopic arrangement of the defect sites, cannot be ruled out. In addition, the fact that SBD can be observed in a thick oxide layer is compatible with the idea that what fundamentally governs the conduction characteristics is the bottleneck of the constriction and not its total length.¹⁶

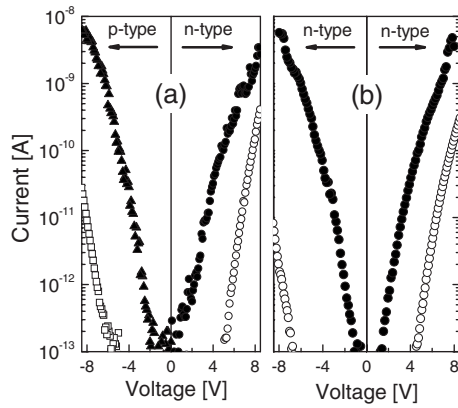


FIG. 4. (a) Effect of the substrate type and (b) effect of the injection polarity, on the SBD current. Open and filled symbols correspond to the fresh and SBD I - V characteristics, respectively.

To complete the electrical characterization of SBD in MgO, the roles played by the substrate type and gate voltage (V_G) polarity were investigated. Figure 4(a) shows that similar SBD currents can be found both for n ($V_G > 0$) and p -type ($V_G > 0$) substrate samples in accumulation conditions. Identical results arise for a n -type Si sample biased with both injection polarities as shown in Fig. 4(b). Notice that this is not the case for the fresh I - V s. These plots indicate that the injecting electrode does not play any major role after SBD as long as there is sufficient charge available for conduction. The current seems to be limited by the breakdown path itself, so that its transmission properties become a key element for the description of the phenomenon.⁵

In summary, the postbreakdown electrical behavior of MgO layers in MOS structures was investigated. It was shown that SBD conduction can be detected in a relatively thick high- κ film and that the connection between the charge transport mechanisms in MgO and SiO₂ goes beyond the fact that they share the same power-law I - V model. The correlation found between the model parameters in both of the materials investigated, and consequently, the apparent simplicity of the underlying physics, should perhaps be regarded as an

indication of the low dimensionality of these conducting structures.

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